ICMSCC-2023

International Conference on **Machine Learning for Signal Processing, Communications** and Control Systems

(Hybrid Mode)



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Geethanjali College of Engineering and Technology (GCET) was established in the year 2005 under the gegis of Teig Educational Society, constituted by a group of academicians and intellectuals. It has highly competent and dedicated faculty providing holistic development of its students. The college has been recognized as a Scientific and Industrial Research Organization (SIRO) by DSIR. The college offers undergraduate programs in engineering branches, namely, Computer Science Engineering (CSE), CSE-AIML (Artificial Intelligence & Machine Learning), CSE-DS (Data Science), CSE-CS (Cyber Security), Civil Engineering, Electrical and Electronics Engineering, Mechanical Engineering, Electronics and Communication Engineering, and postgraduate programs, namely, MBA and M. Tech in Computer Science. The college has been conferred "Autonomous" status by UGC since 2016. GCET is accredited at "A+" grade by NAAC in 2022 and is placed in the rank band (101-150) under NIRF Innovation Ranking-2023. All eligible UG Programs are accredited by NBA. The college is approved by AICTE, affiliated to JNTUH, Hyderabad and certified by ISO 9001:2015. The College has executed research projects to the tune of 15 million Indian Rupees funded by DRDO, DST etc.

About The Department:

The Department of Electronics and Communication Engineering (ECE) was established in the year 2005. Right from its inception it is continuously striving to impart quality education and promoting competitive spirit among students for academic excellence. The department has 12 well equipped laboratories with good infrastructure. The department has qualified, experienced and dedicated faculty.

A good number of technical papers are being published regularly by faculty in various National/International Journals of repute. The department has signed Memoranda of Understanding with various organizations to provide real time training to students in the fields of Advanced Communications, VLSI, Networking and Embedded Systems. The Department has two Centers of Excellence, namely, in VLSI Design and Embedded Systems & IoT, through which intensive training is imparted to the students in core areas. Students' innovative and creative ideas are nurtured and honed under "Srujanastra" Club. The department was sanctioned consultancy and research projects by different funding agencies including SERB-DST, MTRDC (DRDO), RCI (DRDO), VEM Technologies in the allied areas of LIDAR, GNSS and Nanomaterials.

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15th & 16th December 2023

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The long and successful history of Machine Learning finds extensive application in the fields of Signal Detection, Channel Encoding and Decoding, Channel Estimation and Prediction, End to End Communication, **Resource Allocation and intelligent control, specifically** optimal control problems for complex nonlinear systems which violates the principles of linear control systems. The challenge of the day is to develop efficient Machine learning algorithms to address various tasks to learning and generalization.This pertainina international conference ICMSCC aims to bring leading academicians, scientists, research scholars and students together around the world to exchange and share their experiences and research findings on the aspects of experimental results and the theoretical propositions, various use cases pertaining to the applications of machine learning in the specified fields of engineering. It also serves as a prominent multidisciplinary forum for academics, practitioners, and educators to present and debate the most current breakthroughs, trends, and concerns, as well as practical difficulties and solutions in the domains Signal Processing, Communications and Control Systems.

Important Dates:

About The Conference:

Conference Dates: 15-16, December 2023 Website: www.conferenceworld.in, www.jardo.com Paper Submission Deadline: 01, December 2023 Paper Acceptance Decision: After 5 days of submission **Camera Ready Paper submission & Registration Deadline:** within 3 days after acceptance

Mode of Presentation:

• OFFLINE MODE:

AUTHOR CAN ATTEND AND PRESENT THE PAPERS AT THE **COLLEGE CAMPUS**

• ONLINE MODE:

AUTHOR CAN ATTEND AND PRESENT THE PAPERS THROUGH **ONLINE GOOGLE PLATFORM**

Registration & Fee details:

Non-Author Participation (Only for attending the conference): 500 INR Presenting the paper in the conference (Max 3 Authors in a paper): 1000 INR Publication in conference proceeding with ISBN (Max 3 Authors in a paper): 1500 INR

Publication in conference proceeding with ISBN and Publication in Journal: 2000 INR (Peer Reviewed),

8000 INR (Web of Science),

12000 INR (Scopus)

If More than three authors, each additional author will have to pay 500 INR. Page Limit 12, INR 100 will be Charged for each additional page.

All papers will undergo a double blind peer review process and the criteria for acceptance will be based on quality, originality, technical content and relevance.

Conference Tracks:

Authors are solicited to contribute to the conference by submitting articles that illustrate research results, projects, experiment works and industrial experiences that describe significant advances in the following areas, but not limited to:

Track-1: Signal Processing

- Statistical Estimation and Learning
- Adaptive Signal Processing
- **Machine Learning and Pattern Recognition**
- **Signal Processing for Sensing and Sensor Networks**
- **Deep Learning in Signal Processing**
- Wireless Sensor Networking

Track-2: Communications

- **Coding and Modulation**
- Source Coding
- **Telecommunication Networks**
- Information Theory
- **5G Communications and beyond**
- **Channel Estimation and Equalization**
 - **Massive MIMO Communication Systems**
 - **Optical Communication**
 - NOMA/OFDM

Track-3: Control Systems

- **Stochastic Control**
- Non Linear Control
- **Dead Time Systems**
- Human Robot Interaction
- **Discrete Event Systems**
- Adaptive Control Systems
- MIMO Controllers
- **Cascade Control System** ٠
- Fractional Order Proportional-Integral-Derivative (FOPID)

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